

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Number : 10/595,303 Confirmation No.: 7114
Applicant : Roy KNECHTEL
Filed : April 6, 2006
Title : METHOD AND DEVICE FOR SECURE, INSULATED AND
ELECTRICALLY CONDUCTIVE ASSEMBLING OF TREATED
SEMICONDUCTOR WAFERS
TC/Art Unit : 2823
Examiner: : Kevin A. PAREDO

Docket No. : 60291.000048
Customer No. : 21967

MAIL STOP FEE AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO ELECTION/RESTRICTION AND CLAIM AMENDMENT

In response to the Office Action mailed July 11, 2008 ("Office Action"), Applicant respectfully requests entry of the included amendments and allowance of the claims.

Amendments to the Claims begin on page 2.

Remarks/Arguments begin on page 8.